

## Specification

# FABRICATION PROCESS FOR PREPARING RECORDING HEAD SLIDERS MADE FROM SILICON SUBSTRATES WITH SIO<sub>2</sub> OVERCOATS

### BACKGROUND OF THE INVENTION

#### 5    Field of the Invention

The present invention relates generally to fabrication of recording heads of disk drive assemblies, and more particularly to use of Deep Reactive Ion Etching in component fabrication.

#### Description of the Prior Art

10              Conventional magnetic recording head sliders are typically made from wafers of a two-phase ceramic, TiC/Al<sub>2</sub>O<sub>3</sub>, also called Al-TiC. After the thin film processing to prepare the recording heads is performed on the Al-TiC wafers, the sliders are then formed. The sliders are fabricated by cutting, grinding and lapping the wafer made of the above material. Normally, as shown in Figs. 1-2C (Prior Art), the wafer 2 is first cut into 15 pieces which are then cut into the slider bodies 4. The sliders bodies 4 are prepared by diamond sawing in three separate sawing steps: (i) coarse sawing to divide the wafer 2 into smaller pieces, called quadrants 6, (ii) row sawing to separate rows 8 of slider bodies 4 from the wafer quadrants 6; these rows 8 are then lapped, and (iii) slider separation sawing to separate the completed sliders 4 from the rows 8. This process has evolved 20 considerably with time, but still suffers from inherent limitations associated with the

diamond sawing process, such as the fairly wide ‘kerf’ needed for the passage of the blade, edge chipping, generation of surface damage, and generation of loosely-bound particles which may become dislodged in the disk drive. These limitations impact the number of heads which can be put on a finished wafer, the yield of acceptable finished

5       sliders and the reliability of the disk drive itself.

An alternative processing approach is available for Si substrates. This is the process known as DRIE (Deep Reactive Ion Etching) which has been developed during the last 8 years, to give a technique, and associated commercially available tooling, which allows the very deep, high aspect ratio etching of Si. For example, material thicker than 1  
10 mm has been etched with aspect ratios (ratio of depth of cut to width of cut) of about 20. This is a two-stage one process in which the material is etched for a few seconds and then passivated a few seconds, so that at the bottom of the trench being etched, the passivation is removed more rapidly than along the adjacent trench walls, thereby focusing the etching action at this point during the etch cycle. DRIE tools are common, commercially,  
15 and are used in fabricating MEMS (Micro Electromechanical Systems) structures, such as Si-based accelerometers.

Thus there is a need for a fabrication which does not have the disadvantages discussed above caused by mechanical methods of separation such as sawing.

## SUMMARY OF THE INVENTION

A method for fabricating recording head sliders made from silicon substrates, is described according to the preferred method of the present invention. A Silicon wafer with a SiO<sub>2</sub> overcoat is provided, and a layer of material which is resistant to Deep

5      Reactive Ion Etching (DRIE) is deposited on the SiO<sub>2</sub> overcoat. A patterned layer of material which is resistant to Reactive Ion Etching (RIE) is deposited on the layer of DRIE-resistant material to form a primary mask. RIE is used through the primary mask to pattern the SiO<sub>2</sub> overcoat layer and the layer of DRIE-resistant material. The primary mask is then removed to expose the layer of DRIE-resistant material which has now

10     been patterned to form a secondary mask. DRIE is then used through the secondary mask to cut the Si wafer into pieces. Finally, the secondary mask is removed

The present invention thus produces flat-faced, smooth slider bodies which may be of higher yield and number from a single wafer than can be produced by conventional sawing methods.

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It is an advantage of the present invention that sliders can be separated without the wide 'kerf' needed for the passage of the blade in conventional sawing, thus allowing higher densities of sliders on a single wafer.

It is another advantage of the present invention that edge chipping associated with

20     conventional sawing methods is reduced, thus producing higher yields.

It is a further advantage of the present invention that surface damage associated with conventional sawing methods is reduced, thus producing higher yields.

- It is yet another advantage of the present invention that there is decreased generation of loosely-bound particles which may become dislodged in the disk drive than
- 5 in past methods. It thus requires less processing to clean up stray particles, and produces higher yields of acceptable finished sliders and improves the reliability of the disk drive itself.

- It is another advantage of the present invention that there is less irregularity to cut surfaces, so that less polishing and lapping is required at various stages in the
- 10 manufacturing process, thus producing higher throughput.

A further advantage of the present invention is that it allows the patterning and etching of the slider bodies using only one photo-masking step.

- An additional advantage of the present invention is that it also avoids the redeposition of non-volatile, etching reaction products which generally occurs when
- 15 conventional photoresist is used for the RIE processing of oxides.

Yet another advantage of the present invention is that the manufacturing process is simplified, requiring fewer stages of handling, and fixturing, so that the process is more efficient.

- 20 These and other features and advantages of the present invention will no doubt become apparent to those skilled in the art upon reading the following detailed description which makes reference to the several figures of the drawing.

## IN THE DRAWINGS

The following drawings are not made to scale as an actual device, and are provided for illustration of the invention described herein.

- Fig. 1 is a front plan view of a wafer as known in the prior art;
- 5 Figs. 2A-2C show front plan views of the stages in the fabrication of slider bodies as known in the prior art;
- Fig. 3 shows a wafer which is cut by using the present method of fabrication; and
- Figs. 4-8 show cross-sectional views of the stages in the fabrication of slider bodies using the present method of fabrication.

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## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Approaches are being considered in which materials other than the current standard material ( $\text{Al}_2\text{O}_3 + \text{TiC}$  ceramic, or N58) are used as the substrate for building magnetic recording heads. The options are many, but a primary choice is the use of Si as  
15 the alternate substrate material and its oxide,  $\text{SiO}_2$ , for the thick overcoat material which covers the recording head electrical structure and for other insulating layers within the head. There are numerous and diverse advantages for choosing Si, but a substantial one is the ease with which slider bodies, containing the recording head, can be prepared from the finished wafer. The choice of  $\text{SiO}_2$  as the overcoat material is advantageous with  
20 respect to the currently used material, sputtered  $\text{Al}_2\text{O}_3$ . From the slider fabrication

perspective, a great advantage is that it can be dry-etched in a Reactive Ion Etching (RIE) tool. The use of RIE to etch Si<sub>0</sub><sub>2</sub> has been practiced for many years, and dedicated tools are available for this purpose. The RIE processing of Si<sub>0</sub><sub>2</sub> is much more convenient than that used presently for Al<sub>2</sub>O<sub>3</sub> because the reaction product for Si<sub>0</sub><sub>2</sub>, SiF<sub>4</sub>, is volatile, whereas the Al-F compounds are not volatile in the available temperature range. For Al<sub>2</sub>O<sub>3</sub>, the RIE process is much more like a sputter-etch process wherein energetic ions bombard the surface to physically sputter away the Al<sub>2</sub>O<sub>3</sub>. Furthermore, the wall angle which can be achieved in Si<sub>0</sub><sub>2</sub> by the RIE process is much steeper than that for Al<sub>2</sub>O<sub>3</sub>.

The process disclosed here is one which is particularly compatible with the materials and process set used in the fabrication of magnetic recording heads. It allows the patterning and etching of the slider bodies using only one photo-masking step. It also avoids a particular problem observed when conventional photoresist is used for the RIE processing of oxides. Redeposition of non-volatile, etching reaction products can occur. This redeposited material is some tenacious combination of the Si<sub>0</sub><sub>2</sub>, mask material and the chemical byproducts of their interactions with the reaction gas. For example, the redeposition generated using hydrocarbon resist and fluorine-based etching gases is extremely difficult, if not impossible, to remove within the chemical, mechanical and thermal constraints for finished recording head sliders.

When the substrate and overcoat choices are Si and Si<sub>0</sub><sub>2</sub>, respectively, the following procedure allows minimal processing to the finished wafer, and allows an effective RIE process of the Si<sub>0</sub><sub>2</sub> overcoat and Deep Reactive Ion Etching (DRIE) process of the Si substrate to produce flat-faced, smooth slider bodies.

Generally, first, the RIE mask for Si<sub>0</sub><sub>2</sub> is chosen as pattern-plated Ni-Fe (ranging from roughly 0 to 100% Ni). Ni has been shown to function as a very high selectivity mask for Si<sub>0</sub><sub>2</sub> (see Massimi, et al, Jpn. J. Appl. Phys., 38 (1999) pp 6150-6153). It has been observed that very little or no redeposition is associated with Ni-Fe masking of Si<sub>0</sub><sub>2</sub>

- 5 during RIE. Any redeposition which is generated is removed along with the Ni-Fe mask during a chemical etch step.

The high etch selectivity seen for RIE of Si has also been observed in the use of Ni-Fe masks during the DRIE of Si. It would be quite advantageous to be able to use a single mask of Ni-Fe for both the RIE and DRIE processes. However, this is not workable because the DRIE process is slowed down by having small amounts of Ni-Fe mask sputtered on the walls being etched, thereby poisoning the etch process substantially and changing the planarity of the etched walls. It is necessary to find an intermediate material, other than Ni-Fe to use for the DRIE process after RIE of the Si<sub>0</sub><sub>2</sub>.

- 10 One possibility is to use a hydrocarbon resist beneath the Ni-Fe, which upon removal of the Ni-Fe, can be used as the DRIE mask. The thickness of resist required to last through the etch of the 1.2 mm of Si substrate is about 60  $\mu$ m. This would require that the patterned Ni-Fe be used as an etch mask for the patterning of the resist, as well as the Si<sub>0</sub><sub>2</sub> overcoat. Thus, two distinct etching processes would be needed to reach the Si surface, making this a less desirable process. In addition, a manufacturable process for 15 resist stripping has not been demonstrated; tenacious residues are often observed.

A better intermediate material has been found, which is also among the materials set used for recording head manufacture. It has been shown that Al<sub>2</sub>O<sub>3</sub> can be used as an

acceptable material for a DRIE mask; it does not measurably retard or alter the DRIE process. However, it will be noted that these other variations have been contemplated and are all encompassed by the present invention.

Fig. 3 shows a silicon wafer **10** having a SiO<sub>2</sub> overcoat layer **12**, which is to be cut into slider bodies **4** using the present method. This will result in all the pieces being separated during the DRIE process at the same time, rather than being cut into quadrants, lapped and polished, cut into rows, lapped and polished and then cut into individual pieces and lapped and polished further as in the prior art, thus requiring much less handling and processing time, as well as improving yields..

The process for the masking and etching procedure for the fabrication of sliders from a Si+SiO<sub>2</sub> wafer is shown in Figs. 4-8, which are a series of cross-sectional views showing the various layers during the stages of the fabrication process. In the following discussion, preferred dimensions for the applied layers are suggested, but are not to be construed as a limitation. Also, no attempt has been made to draw the layer thicknesses in proper scale with each other. Reference is made to all of Figs. 4-8 in the following discussion.

The Si wafer **10** is provided and the SiO<sub>2</sub> layer **12** is deposited, followed by a layer of DRIE-resistant material **14**, preferably sputtered Al<sub>2</sub>O<sub>3</sub>, as shown in Fig. 4. The thickness of this DRIE-resistant layer **14** is preferably 1.5μm (1.5 X 10<sup>-6</sup> meters).

Next, a layer of material that is resistant to RIE **16**, preferably a metal, such as NiFe, is deposited as a seedlayer (not shown) to allow the electroplating of the primary

masking metal layer, which will follow. Photo-resist (also not shown) is then applied, exposed and developed to allow pattern-plating to cover the slider areas, and the pattern-plated layer of RIE-resistant material **16** is applied to a depth of preferably about 2  $\mu\text{m}$ .

The photo-resist is then stripped, producing the primary mask **18**, which may also be

- 5 referred to as an RIE mask **20**. This structure is shown in Fig. 5. NiFe is used as an example here, but any metal, especially any of the transition metals, (i.e. Sc-Zn on the periodic table) can be used. Of these, Cu has also proved especially useful.

Next, Reactive Ion Etching (RIE) is used to remove material which is not protected by the primary mask **18**. The primary mask **18** thus reproduces its pattern in  
10 the underlying layers, namely the 1.5  $\mu\text{m}$  DRIE-resistant layer **14** plus the 30-40  $\mu\text{m}$  SiO<sub>2</sub> overcoat layer **12**, and possibly a bit of the Si layer **10** itself, producing the structure shown in Fig. 6. Part of the pattern which is reproduced from the primary mask **18** into the underlying layers will become a secondary mask **22**, which has now been formed in the DRIE-resistant layer **14**.

15 Optionally, the masking metal seedlayer is sputter etched away before the RIE process above, but it is also possible that this seedlayer may be removed during the RIE process, since it is so thin.

The masking metal layer is then selectively removed by wet-etching which leaves the DRIE-resistant layer **14**, now the secondary mask **22**, intact. This secondary mask **22**  
20 is used for the Deep Reactive Ion Etching (DRIE) process which follows and may also be referred to as a DRIE mask **24**. The structure of the layers just before the DRIE process commences is shown in Fig. 7.

The remaining DRIE-resistant layer is then used to mask the underlying layers **10**, **12** from the DRIE. The Si wafer **10** is cut completely through, and the DRIE-resistant layer (now not shown) is then removed using wet etching. The sliders **26** are thus separated from each other without the use of sawing, and also without the lapping 5 previously required to remove the saw abrasions from the pieces. The separated sliders **26** are shown in Fig. 8.

The present invention thus produces flat-faced, smooth slider bodies which may be of higher yield and number from a single wafer than can be produced by conventional sawing methods.

10 While the present invention has been shown and described with regard to certain preferred embodiments, it is to be understood that modifications in form and detail will no doubt be developed by those skilled in the art upon reviewing this disclosure. It is therefore intended that the following claims cover all such alterations and modifications that nevertheless include the true spirit and scope of the inventive features of the present 15 invention.

## **FABRICATION PROCESS FOR PREPARING RECORDING HEAD SLIDERS**

**15                  MADE FROM SILICON SUBSTRATES WITH SIO<sub>2</sub> OVERCOATS**

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AND INFORMATIONAL PURPOSES ONLY, AND DOES NOT FORM A PART OF  
20                  THE FORMAL PATENT APPLICATION.**

- |              |                         |
|--------------|-------------------------|
| 2            | wafer                   |
| 4            | slider bodies           |
| 6            | quadrant                |
| 25        8  | rows                    |
| 10           | Si wafer                |
| 12           | SiO <sub>2</sub> layer  |
| 14           | DRIE-resistant material |
| 16           | RIE-resistant material  |
| 30        18 | primary mask            |
| 20           | RIE mask                |
| 22           | secondary mask          |
| 24           | DRIE mask               |
| 26           | separated sliders       |